



- •1GS/s Timing, 100MHz State, 16 Channel Logic Analyser.
- •Simultaneous Multi-Protocol Analysis supports: I2C, SPI, UART and I3C-SPMI-RFFE.
- •Advanced trigger capabilities including Protocol aware Triggers.
- Easy to use software.
- •Smart continuous streaming of data between Logic Analyser and host computer via USB 3.0 interface.
- •Effectively solves embedded hardware and Software integration challenges.

Solving Embedded Hardware Issues

- •16 Channels Logic Analyser with timing and state analysis capability.
 - •100MHz Synchronous state analysis helps identify embedded design signal behavior.
 - •Asynchronous timing analysis helps identify glitches.
- •Debugging timing issues in hardware with 1GS/s per channel timing acquisition.
- •Delay and Pulse width trigger feature to capture elusive hardware design issues.

Solving Embedded Software Issues

- •Simultaneous Multi-Protocol support: I2C, SPI, UART and I3C-SPMI-RFFE.
- •System level insights to protocol messages.
- Multiple markers to resolve protocol message timing issues.









